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driver and SOI

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1 **A high-voltage monolithic isolator for a communication network in**
Akiyama, N.; Kojima, Y.; Nemoto, M.; Yukutake, S.; Iwasaki, T.; Amishiro, I
Kanekawa, N.; Watanabe, A.; Takeuchi, Y.
Electron Devices, IEEE Transactions on , Volume: 49 Issue: 5 , May 2002
Page(s): 895 -901

[Abstract]   [PDF Full-Text (318 KB)] **JNL**

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2 **A 744mW adaptive supply full-rate ADSL CO driver**
Pierdomenico, J.; Wurcer, S.; Day, B.
Solid-State Circuits Conference, 2002. Digest of Technical Papers. ISSCC. 2(
International , Volume: 1 , 2002
Page(s): 320 -470 vol.1

[Abstract]   [PDF Full-Text (494 KB)] **CNF**

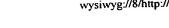3 **A 1.2Gbps SOI-BiCMOS write driver for hard disk drives**
Yoshizawa, H.; Kobayashi, Y.; Yoshinaga, M.; Ookuma, Y.; Maio, K.; Inkura,
Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002
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[Abstract]   [PDF Full-Text (351 KB)] **CNF**

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4 **A low loss high-frequency half-bridge driver with integrated powei using EZ-HV SOI technology**
Li, F.; Giannopoulos, D.; Wacyk, I.
Applied Power Electronics Conference and Exposition, 2002. APEC 2002. Sev
Annual IEEE , Volume: 2 , 2002

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1 **A microprocessor with a 128-bit CPU, ten floating-point MAC's, f u floating-point dividers, and an MPEG-2 decoder**
Suzuoki, M.; Kutaragi, K.; Hiroi, T.; Magoshi, H.; Okamoto, S.; Oka, M.; Ohl Yamamoto, Y.; Furuhashi, M.; Tanaka, M.; Yutaka, T.; Okada, T.; Nagamats Urakawa, Y.; Funyu, M.; Kunimatsu, A.; Goto, H.; Hashimoto, K.; Ide, N.; M H.; Ohtagu
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[Abstract]   [PDF Full-Text (640 KB)] **JNL**

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2 **A flexible processor architecture for MPEG-4 image compositing**
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Acoustics, Speech and Signal Processing, 1998. Proceedings of the 1998 IEE International Conference on , Volume: 5 , 1998
Page(s): 3153 -3156 vol.5

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3 **A three-dimensional high-throughput architecture using through-w optical interconnect**
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Page(s): 1085 -1092

[Abstract]   [PDF Full-Text (820 KB)] **JNL**

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4 **Low-power and high-stability SRAM technology using a laser-recry**